

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/729088	CONT/PRIOR F	CLASS 257 716	SUBCLASS 12	ART UNIT 88-11 25	EXAMINER 11/omPSON
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Hiroomi Nakao

Method of designing a semiconductor circuit and a semiconductor circuit designed using the method

PTO-2040
12/89

ISSUING CLASSIFICATION

ORIGINAL					CROSS REFERENCE(S)									
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)								
INTERNATIONAL CLASSIFICATION														

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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